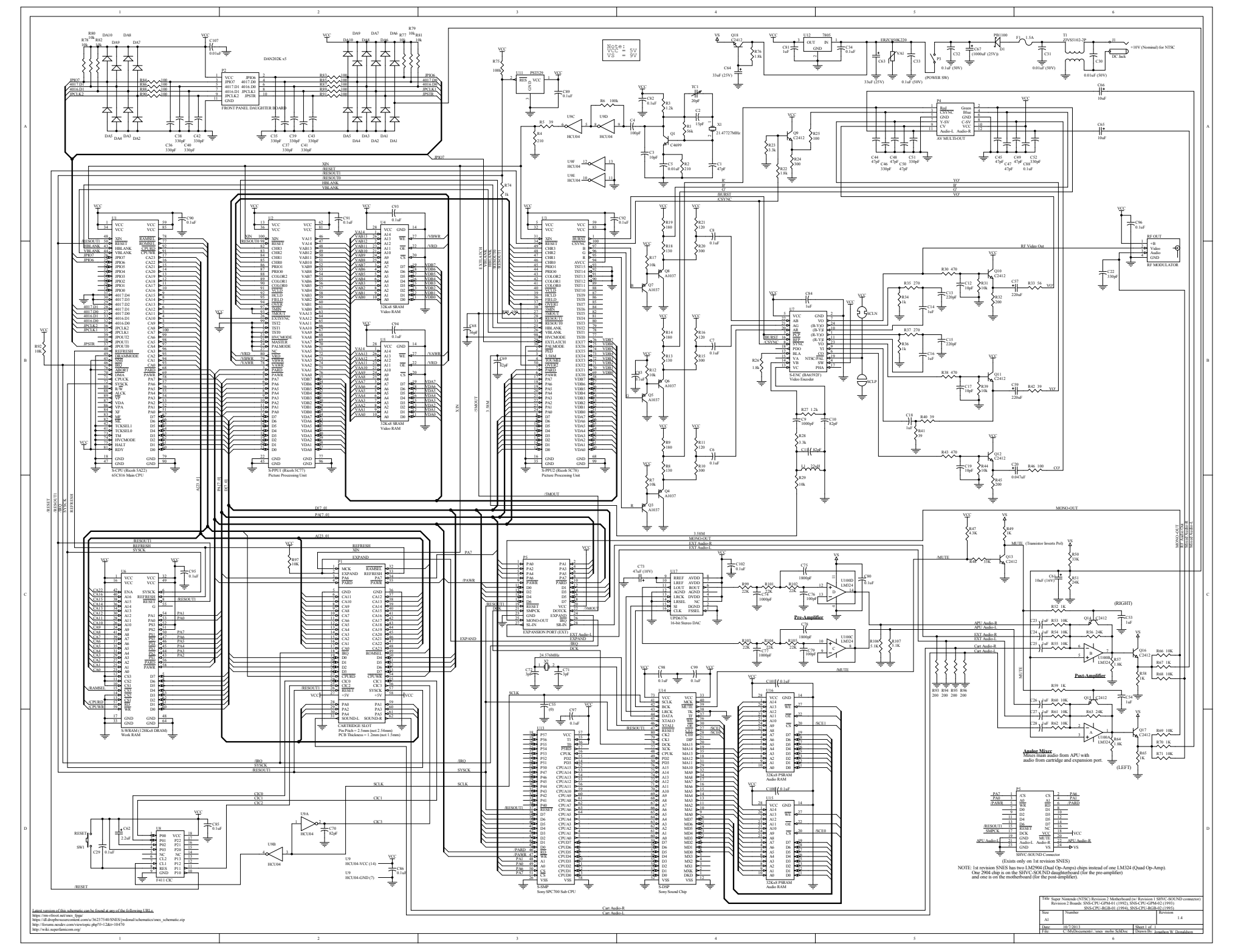
Note:
Vcc = 5V
Vs = 9V
S-CPU (Ricoh 5A22)
65C816 Main CPU
S-PPU1 (Ricoh 5C77)
Picture Processing Unit
S-PPU2 (Ricoh 5C78)
Picture Processing Unit
32Kx8 SRAM
Video RAM
S-WRAM (128Kx8 DRAM)
Work RAM
S-SMP
Sony SPC700 Sub CPU
S-DSP
Sony Sound Chip
32Kx8 PSRAM
Audio RAM
S-ENC (BA6592F)
Video Encoder
UPD6376
16-bit Stereo DAC
F411 CIC
FRONT PANEL DAUGHTER BOARD
DAN202K x5
AV MULTI-OUT
RF MODULATOR
DC Jack
(POWER SW)
Pre-Amplifier
Post-Amplifier
Analog Mixer
Mixes main audio from APU with audio from cartridge and expansion port.
SHVC-SOUND Connector
(Exists only on 1st revision SNES)
NOTE: 1st revision SNES has two LM2904 (Quad Op-Amps) chips instead of one LM324 (Quad Op-Amp).
One 2904 chip is on the SHVC-SOUND daughterboard (for the pre-amplifier)
and one is on the motherboard (for the post-amplifier).
Latest version of this schematic can be found at any of the following URLs:
https://rm-rf.root.net/snes_fpga/
https://dl.dropboxusercontent.com/u/36223740/SNES/jvadonal/schematics/snes_schematic.zip
http://forums.nesdev.com/viewtopic.php?f=12&t=10470
http://wiki.superfamicom.org
Title: Super Nintendo (NTSC) Revision 2 Motherboard (w/ Revision 1 SHVC-SOUND connector)
Revision 2 Boards: SNS-CPU-GPM-01 (1992), SNS-CPU-GPM-02 (1993)
SNS-CPU-BGB-01 (1994), SNS-CPU-BGB-02 (1995)
Size: A1
Number:
Revision: 1.4
Date: 10/7/2013
Sheet 1 of 1
File: C:\MyDocuments\...\snes_mobo_SMDec
Drawn By: Jonathan W. Donaldson